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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/605,466	10/01/2003	Wagdi W. Abadeer	BUR920030053US1	2465
21918	7590 05/13/2005		EXAMINER	
DOWNS RA	ACHLIN MARTIN PL	CUNNINGHAM, TERRY D		
P O BOX 190			ART UNIT	PAPER NUMBER
BURLINGTO	ON, VT 05402-0190		2816	
			DATE MAILED: 05/13/2009	5

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/605,466	ABADEER ET AL.				
Office Action Summary	Examiner	Art Unit				
	Terry D. Cunningham	2816				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status		·				
1) Responsive to communication(s) filed on 31 M	arch 2005.					
2a) ☐ This action is FINAL . 2b) ☑ This						
3) Since this application is in condition for allowar	nce except for formal matters, pro	secution as to the merits is				
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Disposition of Claims						
4) Claim(s) <u>1-20</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdraw	vn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-20</u> is/are rejected. 7)□ Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/o	r election requirement					
o, are subject to restriction and/o	r cicolon requirement.					
Application Papers						
9)☐ The specification is objected to by the Examine	r.					
10)⊠ The drawing(s) filed on <u>01 October 2003</u> is/are:	a)⊠ accepted or b)□ objected	I to by the Examiner.				
Applicant may not request that any objection to the	-,,	` '				
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	-, -, -, -, -, -, -, -, -, -, -, -, -, -	•				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:	priority under 35 U.S.C. § 119(a))-(d) or (f).				
1. Certified copies of the priority documents	s have been received.					
2. Certified copies of the priority documents		on No				
3.☐ Copies of the certified copies of the prior						
application from the International Bureau	· · · · · · · · · · · · · · · · · · ·					
* See the attached detailed Office action for a list	of the certified copies not receive	ed.				
Attachment(s) 1) Notice of References Cited (PTO-892)	A) 🔲 Internitorio Como	(DTO 442)				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4)	ate				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	5) Notice of Informal P 6) Other:	atent Application (PTO-152)				
S. Patent and Trademark Office		· · · · · · · · · · · · · · · · · · ·				

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DETAILED ACTION

Summary of changes in this action

1. Examiner has considered Applicant's rejections in view of Arakawa and has found them to be persuasive. The rejection was based on Fig. 5 showing symbols for C₁ and C₂ that are conventionally known to represent capacitor-connected MOSFETs. However, the specification of Arakawa does not expressly disclose this to be fact. Additionally, the MOS structure shown in Fig. 6 does not clearly have both a "drain" and a "source". Thus, this rejection is hereby removed. Examiner notes that this reference may later be used in a rejection under 35 U.S.C. § 103.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-20 are also rejected under 35 U.S.C. 102(e) as being anticipated by Otsuka (USPN 6,737,912).

With respect to claims 1-11 and 16-18, Otsuka discloses, in Fig. 2-4, a circuit comprising: "a first transistor (42)"; and "a second transistor (46)", all connected and operating similarly as recited by Applicant. The reference to Otsuka expressly discloses that there is a ratio between

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the gate areas of N50 and N51 to determine the division factor of the divider (e.g., see Col. 5, lines 63 to Col. 6, line 12).

With respect to claims 12-15, 19 and 20 are, clearly the above circuit to Otsuka will provide the recited method. The reference to Otsuka expressly provides an example of the ratio being 1 (i.e., the gate electrodes areas are "equal"). Reference is made to Col. 5, lines 59-62.

Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Majid et al. (USPN 6,518,814).

With respect to claims 1-11 and 16-18, Majid et al. discloses, in Figs. 5 and 7, a circuit comprising: "a first transistor (N50) including a gate electrode (16 of Fig. 3 as shown in Fig. 3) having an area, a source (one of 44 of Fig. 3) and a drain (another of 44 of Fig. 3)"; and "a second transistor (N51 as shown in Fig. 4) including a gate electrode (44 of Fig. 4) having an area, a source (one of the terminals of Fig 4 connected to ground) and a drain (another of the terminals of Fig 4 connected to ground)", all connected and operating similarly as recited by Applicant. The reference to Majid et al. expressly discloses that the capacitors are of different size.

With respect to claims 12-15, 19 and 20 are, clearly the above circuit to Majid et al. will provide the recited method.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Terry Cunningham whose telephone number is 571-272-1742. The examiner can normally be reached on Monday-Thursday from 7:30 to 6:00.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TC May 11, 2005 Terry D. Cunningha Primary Examiner Art Unit 2816